

S9	2	("5867399").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/08 13:58
S10	2	("6144954").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/08 16:06
S11	373	licensing and auction	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 16:07
S12	19	licensing and auction and (product adj development)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 16:07
S13	12666	(multiplex\$5 or mux\$5) and verif\$5 and (chip or soc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:02
S14	6411	((I/O or (input and output)) same (multiplex\$5 or mux\$5)) and verif\$5 and (chip or soc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:02
S15	3639	((I/O or (input and output)) same (multiplex\$5 or mux\$5)) and verif\$5 and (chip or soc) and pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:02
S16	1215	((I/O or (input and output)) same (multiplex\$5 or mux\$5)) and (verif\$5 same (chip or soc))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:03

S17	765	((I/O or (input and output)) same (multiplex\$5 or mux\$5)) and (verif\$5 same (chip or soc)) and controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:03
S18	253	((I/O or (input and output)) same (multiplex\$5 or mux\$5)) and (verif\$5 same (chip or soc)) and controller and (operating adj system)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:31
S19	4	(soc and pin and verif\$5).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:32
S20	725	(pin and verif\$5).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:32
S21	7	(pin and verif\$5).ab. and soc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:32
S22	125	(pin and verif\$5).ab. and (soc or chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:46
S23	72	synplicity	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:46
S24	0	synplicity and duolog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:47

S25	74	synplicity or duolog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:47
S26	22	(synplicity or duolog) and pin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:47
S27	21	(synplicity or duolog) and I/O	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:47
S28	10	(synplicity or duolog) and I/O and verif\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:49
S29	22	(synplicity or duolog) and verif\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:49
S30	20	(synplicity or duolog) and valid\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:49
S31	34	(synplicity or duolog) and (valid\$7 or verif\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:51
S32	5	(synplicity or duolog) and testbench	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:57

S33	109639	(soc or chip) and (valid\$7 or verif\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:57
S34	4957	(soc or chip) and (valid\$7 or verif\$7) and (pin same (map\$5 or mux\$5 or multiplex\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:59
S35	1916	(soc or chip) and (valid\$7 or verif\$7) and (pin near5 (map\$5 or mux\$5 or multiplex\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:59
S36	1660	(soc or chip) and (valid\$7 or verif\$7) and (pin near4 (map\$5 or mux\$5 or multiplex\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:59
S37	1387	(soc or chip) and (valid\$7 or verif\$7) and (pin near3 (map\$5 or mux\$5 or multiplex\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 12:59
S38	998	(soc or chip) and (valid\$7 or verif\$7) and (pin near2 (map\$5 or mux\$5 or multiplex\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 13:00
S39	13	((soc or chip) and (valid\$7 or verif\$7)).ab. and (pin near2 (map\$5 or mux\$5 or multiplex\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 13:05
S40	846	(703/14).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/12 13:07

S43	0	S40 and @py<"20020201".pd.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 13:10
S44	314	S40 and @py<"2002"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/12 13:10

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